



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Amr M. MOHSEN

Serial No.: 08/632,298

Filed: April 12, 1996

For: Field Programmable Printed Circuit  
Boards

Atty. Docket No.: 003921.00258

Group Art Unit: 2304

Examiner: Hugh M. Jones

Confirmation No. 6358

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

The Honorable Commissioner for Patents  
Customer Service Window  
Randolph Building  
401 Dulany Street  
Alexandria, VA 22314

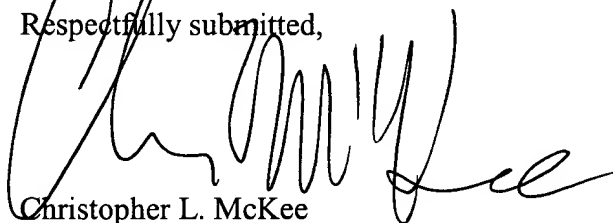
Sir:

Pursuant to 37 C.F.R. §1.56, the attention of the U.S. Patent and Trademark Office is, hereby directed to the references listed in the attached Forms PTO/SB/08. Pursuant to the Office's October 12, 2004 OG Notice, it is understood that 37 C.F.R. §1.98 no longer requires submission of a copy of each cited U.S. patent and published patent application, unless such copies are specifically required by the Office. Accordingly, a copy of the U.S. patents and published applications is omitted. A copy of the other listed references is enclosed.

Applicant submits this information so that the Patent and Trademark Office may, in the first instance, determine any materiality thereof to the claimed invention. It is respectfully requested that the information be formally considered during the prosecution of this application, and that the listed references appear among the "References Cited" on any patent to issue herefrom.

The present Information Disclosure Statement is being filed after the mailing date of the first Office Action on the merits, but before a Final Office Action or Notice of Allowance (following withdrawal of the case from issue). Pursuant to 37 CFR §1.97(c)(2), this Statement is accompanied by the \$180.00 fee specified in 37 C.F.R. §1.17(p). If there is any discrepancy in the fee, the Commissioner is authorized to charge our Deposit Account No. 19-0733.

Respectfully submitted,



Christopher L. McKee  
Registration No. 32,384

BANNER & WITCOFF, LTD.  
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Date: \_\_\_\_\_

3/31/06



Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 1 of 4

**Complete if Known**

Application Number	08/632,298
Filing Date	April 12, 1996
First Named Inventor	Amr M. MOHSEN
Art Unit	2304
Examiner Name	Hugh M. Jones
Attorney Docket Number	003921.00258

**U.S. PATENT DOCUMENTS**

Examiner Initials *	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code <sup>2</sup> (if known)			
		3,849,872	11/1974	Hubacher	
		4,183,460	01/1980	Yuen et al.	
		4,357,703	11/1982	Van Brunt	
		4,458,197	07/03/1984	Robinson	
		4,578,773	03/1986	Desai et al.	
		4,697,241	09/1987	Lavi	
		4,729,124	03/1988	Hansel et al.	
		4,744,084	05/1988	Beck et al.	
		4,855,253	08/1989	Weber	
		4,956,602	09/1990	Parrish	
		4,961,053	10/1990	Krug	
		5,144,548	9/01/1992	Salandro	
		5,260,664	11/1993	Graham	
		5,371,390	12/1994	Mohsen	

**FOREIGN PATENT DOCUMENTS**

Examiner Initials *	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> (if known)				
		GB 2 137 811 A	10/10/1984	International Rectifier Corp.		
		JP 63-49831	02/03/1988	Matsushita Electric Industrial Co., Ltd.		
		GB 2 180 382 A	03/25/1987	Pilkington Micro-Electronics Limited		
		WO 87-02827	05/07/1987	4C Electronics, Inc.		
		JP 58-117,469	07/13/1983			Partial

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Sheet	2	of	4
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<i>Attorney Docket Number</i>	003921.00258

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STATEMENT BY APPLICANT**

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Sheet 3 of 4

**Complete if Known**

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First Named Inventor	Amr M. MOHSEN
Art Unit	2304
Examiner Name	Hugh M. Jones
Attorney Docket Number	003921.00258

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		Brown, S. et al., "A Detailed Router for Field Programmable Gate Arrays" pp. 382-85	
		Greene, J. et al., "Segmented Channel Routing," 27th ACM/IEEE Design Automation Conference, June 24-28, 1990, pp. 567-72	
		Lin et al., "SILK: A Simulated Evolution Router," IEEE Transactions on Computer Design, Vol. 8, No. 10, October, 1989, pp. 1108-14	
		Wynn, Pardner, "Designing With Logic Cell Arrays," p. 1-9.	
		Wynn, Pardner, "In-Circuit Emulation for ASIC-Based Designs," VLSI Systems Design, October 1986, pp. 38-45	
		Agrawal, P. et al., "Architecture and Design of the MARS Hardware Accelerator," 24th ACM/IEEE Design Automation Conference, 1987, pp. 101-07	
		Blodgett, Albert, "A Multilayer Ceramic Multichip Module," IEEE Transactions on Components, Hybrids and Manufacturing Technology, vol. CHMT-3, No.4, December 1980, pp. 634-37	
		Seraphim, D.P. and Feinberg, I., "Electronic Packaging Evolution in IBM," IBM J. Res. Develop., Vol. 25, No. 5, September 1981, pp. 617-29	
		Palesko, C. and Akers L., "Logic Partitioning for Minimizing Gate Arrays," IEEE Transactions on Computer Aided Design of Integrated Circuits and Systems, Vol. CAD-2, No. 2, April 1983, pp. 117-21	
		Stroud, C. and Munoz, R., :CONES: A System for Automated Synthesis of VLSI and Programmable Logic from Behavioral Models," IEEE International Conference on Computer Aided Design, November 11-13, 1986, pp. 428-31	
		The Programmable Gate Array Design Handbook, Xilinx, Inc., 1986	
		Microelectronics Packaging Handbook, 1989	
		Circuits, Interconnections and Packaging for VLSI, 1990	
		Handbook of Microelectronics Packaging and Interconnection Technologies, Chapter 1, 1985	
		Handbook of Microelectronics Packaging and Interconnection Technologies, Chapter 7, 1985	

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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use as many sheets as necessary)		Application Number	08/632,298
		Filing Date	April 12, 1996
		First Named Inventor	Amr M. MOHSEN
		Art Unit	2304
		Examiner Name	Hugh M. Jones
Sheet 4 of 4	Attorney Docket Number	003921.00258	

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		Kit Offers Instant Insights, Electronics, January 23, 1975, p. 70	
		Horowitz and Hill, The Art of Electronics, Chapter 12 [copyright 1980 and 1989]	
		HP-12C Owner's Handbook and Problem Solving Guide, Hewlett-Packard Company, Palo Alto, CA, 1981, pp. 220-221	
		IBM Tech. Discl. Bul., "Logically Controlled Chip Interconnection Technique," Aug. 1989, Vol. 32, No. 3B, pp. 294-299	
		"Synthesis of Logic Functions on an Array of Integrated Circuits," Contract Report AFCRL-66-298, UNIVAC Division of Sperry Rand Corp., Blue Bell, PA, Oct. 31, 1965.	

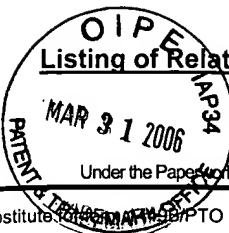
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# **Listing of Related Litigation Papers Submitted Responsive to Request for Information**



PTO/SB/08b(08-03)

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## **INFORMATION DISCLOSURE STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 1 of 2

### **Complete if Known**

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		U.S. District Ct. (N.D. Cal.) Civil Docket for Case # 3:04-cv-01522-EDL ( <i>Aptix Corp. v. Willmore et al.</i> )	
		Complaint filed in <i>Aptix Corp. v. Willmore et al.</i>	
		U.S. District Ct. (N.D. Cal.) Civil Docket for Case # 3:98-cv-00762-WHA ( <i>Aptix Corp. et al. v. Quickturn Design Systems</i> )	
		Findings of Fact and Conclusions of Law After Evidentiary Hearing on Motion for Terminating Sanctions in <i>Aptix Corp. v. Quickturn Design Systems, Inc.</i> , Case No. 98-00762, 2000 WL 852813 (N.D. Cal. 2000)	
		<i>Aptix Corp. v. Quickturn Design Systems, Inc.</i> , 13 Fed.Appx. 972 (Fed. Cir. 2001) (Rule 36 affirmance)	
		<i>Aptix Corp. v. Quickturn Design Systems, Inc.</i> , 269 F.3d 1369 (Fed. Cir. 2001)	
		<i>Aptix Corp. v. Quickturn Design Systems, Inc.</i> , 148 Fed.Appx. 924 (Fed. Cir. 2005)	
		Joint Claim Construction Statement, in <i>Aptix Corp. v. Quickturn Design Systems</i> (Docket # 45)	
		Aptix's and Meta's Memorandum on Claim Construction, in <i>Aptix Corp. v. Quickturn Design Systems, Inc.</i> (Docket # 266)	
		Declaration of Dr. Harry L. Tredennick III in Support of Plaintiffs' Memorandum on Claim Construction in <i>Aptix Corp. v. Quickturn Design Systems</i> (Docket # 268)	
		Response by Aptix and Meta to Quickturn's Memorandum on Claim Construction, in <i>Aptix Corp. v. Quickturn Design Systems</i> (Docket # 297)	
		Tentative Ruling After Claim Construction Hearing, in <i>Aptix Corp. v. Quickturn Design Systems</i> (Docket # 314)	

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Sheet 2 of 2

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Attorney Docket Number	003921.00258

		Aptix's Memorandum Re Comparison of Claims of '638 and '069 Aptix Patents, in <i>Aptix Corp. v. Quickturn Design Systems</i> (Docket # 316)	
		Final Ruling After Claim Construction Hearing, in <i>Aptix Corp. v. Quickturn Design Systems</i> (Docket # 352)	
		Quickturn's Closing Brief for Evidentiary Hearing, in <i>Aptix Corp. v. Quickturn Design Systems</i> (Docket # 456)	
		Plaintiff Aptix Corporation's Post-Hearing Brief, in <i>Aptix Corp. v. Quickturn Design Systems</i> (Docket # 457)	
		Meta and Mentor Graphics' Post-Evidentiary Hearing Brief, in <i>Aptix Corp. v. Quickturn Design Systems</i> (Docket # 459)	
		Notice of Motion and Motion for Summary Judgment that U.S. Patent 5,544,069 is Invalid for Failure to Comply with 35 U.S.C. §112, in <i>Aptix Corp. v. Quickturn Design Systems</i> (Docket # 475)	
		Declaration of Jeffrey A. Miller in Support of Quickturn's Motion for Summary Judgment that U.S. Patent 5,544,069 is Invalid for Failure to Comply with 35 U.S.C. §112, in <i>Aptix Corp. v. Quickturn Design Systems</i> (Docket # 478)	
		Plaintiffs' Notice of Motion and Motion for Summary Judgment on Quickturn's (1) Inequitable Conduct Defense; (2) Inequitable Conduct Counterclaim; and (3) Derivation Defense; Points and Authorities in Support, in <i>Aptix Corp. v. Quickturn Design Systems</i> (Docket # 480)	
		Quickturn's Petition to File its Motion Re: Derivation and Memo in Support Thereof One Day Late, in <i>Aptix Corp. v. Quickturn Design Systems</i> (Docket # 485)	
		[Proposed] Order Granting Quickturn's Motion for Summary Judgment of Invalidity of Mohsen U.S. patent No. 5,544,069 Under 35 U.S.C. §102(f), in <i>Aptix Corp. v. Quickturn Design Systems</i>	

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